

Electronic Acknowledgement Receipt

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Application Number:	10791337
International Application Number:	
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Title of Invention:	Spacer-less transistor integration scheme for high-k gate dielectrics and small gate-to-gate spaces applicable to Si, SiGe strained silicon schemes
First Named Inventor/Applicant Name:	David Pritchard
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		LSI1P240_AMEND_C.pdf	352749	yes	8

Multipart Description/PDF files in .zip description							
Document Description		Start	End				
Amendment - After Non-Final Rejection		1	1				
Claims		2	4				
Applicant Arguments/Remarks Made in an Amendment		5	8				
Warnings:							
Information:							
2	Miscellaneous Incoming Letter	LSI1P240_AMEND_C_TRA NS.pdf	50143	no	1		
Warnings:							
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Total Files Size (in bytes):				402892			
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